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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant : Jaime A. Segura et al.      Art Unit : Unknown  
Serial No. : 10/024,467      Examiner : Unknown  
Filed : December 17, 2001  
Title : CMOS RADIATION-MEASURING CIRCUIT WITH A VARIABLE  
THRESHOLD

**Attention: Official Draftsman**  
Commissioner for Patents  
Washington, D.C. 20231

**TRANSMITTAL OF FORMAL DRAWINGS**

Please substitute the enclosed one (1) sheet of formal drawings (Figure 1 and Figure 2)  
for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 4-19-02

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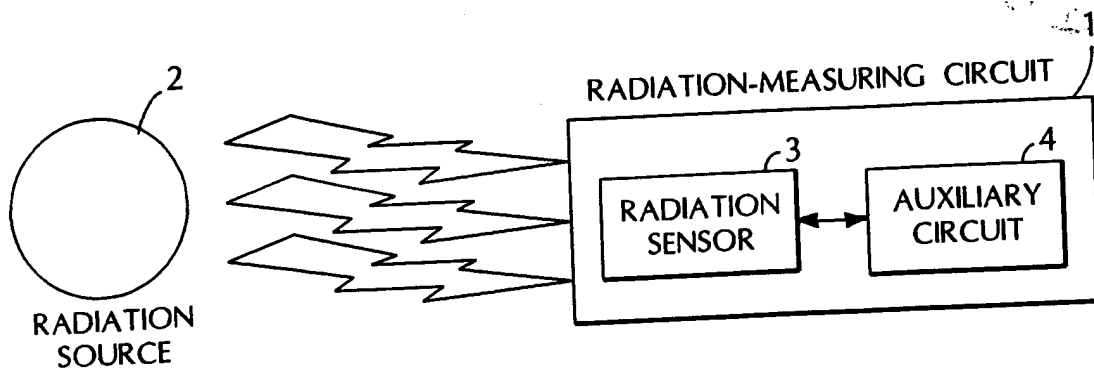


FIG. 1

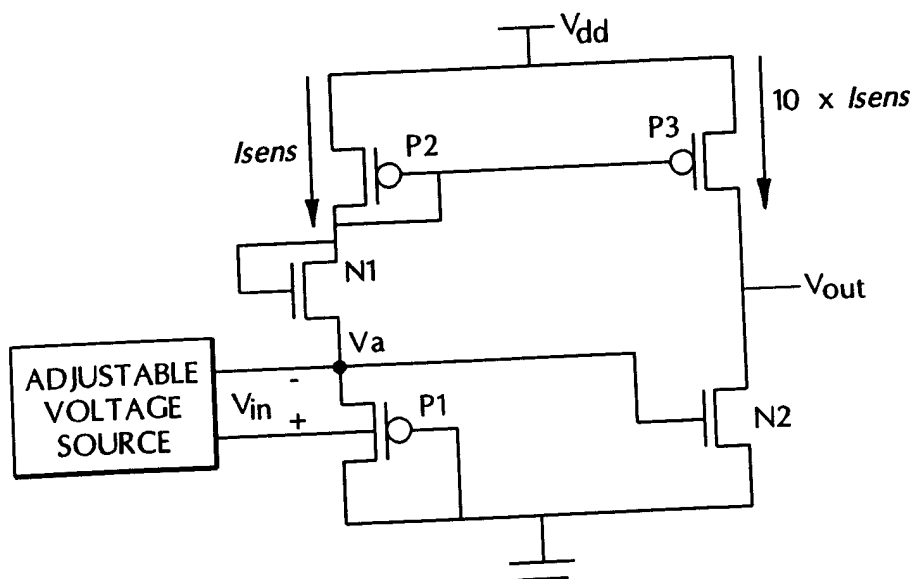


FIG. 2